

- 1. Integration and mechanical issues
- 2. Sensors
- 3. Preamp-shaper chips
- 4. Hybrids
- 5. Read-out strategy
- 6. FMD-TDR



#### Forward detector overview





#### Beam and FMD support cone RB26 side











#### Finite element calculations of beam pipe support







Prototype beam-pipe support structure (crucial for LHC) in construction at NBI

### **Installation Sequence**







LHC Comp. Review, 23 Mar 2004

Jens Jørgen Gaardhøje, NBI

4

### **FMD** sensors



Ø150

Ø134

279.9



Final negotiations with (Micron/Hamamatsu) ongoing.

#### Outer module

LHC Comp. Review, 23 Mar 2004



### **Custom preamp-shaper chip**



VA1\_ALICE

50 µm input pitch



ond dvdd dyss delay adjust delay on holdb hold (dummy)

dresetb (dummy) shift in b ck (dummy)

> ckb shift out b

avss pre bias

**Radiation hardness** > 500-1000 krad **Peaking time** 0.7-2.0 µsec (optimised at 1.2-1.4 µsec) < 500 e- (= 0.02 MIP) Noise (ENC) **Capacitance matching** 5-25 pF 0-20 MIPs (or ±10 MIPS) **Dynamic range** 128 channels per chip **Highly integrated Read-out speed** ~ 10 MHz included Test and calibration circuits **Power consumption** 0.6 mW per channel **Compatibility with** requires level shift **ALTRO** VA1' useful Early prototype Affordable cost yes **Channel count** 51,200 (400 chips à 128 channels)

### VA1\_ALICE noise simulations





LHC Comp. Review, 23 Mar 2004

## **Hybrids**







# **Radiation environment**



#### Doses and Fluences in Central ALICE (10 years running):



LHC Comp. Review, 23 Mar 2004



#### LHC Comp. Review, 23 Mar 2004

### **New RCU location**







# Estimates of SEU



High Energetic Hadron Flux (@ TPCin): 250 - 550 hadrons / sec · cm<sup>2</sup>

	Error rate per run (4 hours) per device
FEC	3•10-4
RCU	1.5•10 <sup>-2</sup>
DCS	3•10 <sup>-2</sup>



#### FMD and TDR milestones Detailed schedule for 2004



Date	FMD project	FMD-TDR	TDR-overall
March 2004	Decide RCU placement	Basic text available	Basic FMD text available
April 2004	Decide Si manufacturer Design digitizer test board Order Si prototype	Rework figures Expand text	Basic TO and V0 text available.
May 2004	Start digitizer test board construction Order Hybrid prototype	Finalize text and figures.	Completed FMD text. Work on TO, VO text and figs
June 2004	Delivery hybrid prototype Test hybrids	FMD text and figures completed	TO,VO,FMD text and figures completed
July 2004	Test hybrids and digitizer board prototypes		Editing of TDR, proof reading etc.
August 2004	Delivery Si prototype New Post. Doc for the project.		Final proof reading. Decide layout/cover/photos
September 2004	Gluing Si + hybrids Bonding at CERN		Printing of TDR
October 2004	Test: Si+hybrid+digitizer+RCU		
November 2004	Tests in-beam: 680MeV		
December 2004	Decide on final order Si+hybrids		



# Overall schedule 2004-2006



Date	FMD project
Spring 2004	Order Si and FEE Hybrid prototypes (IDEAS)
	Design and construct Digitizer board (NBI)
Fall 2004	Bonding Prototype Si and Hybrids
	Test: Si+Hybrid+digitizer+RCU with e- beam
End 2004	Decide placement of final order
Jan 2005	Test with HI beam at RHIC
	Place production order
May 2005	Delivery of all Si and hybrids
August 2005	Gluing Si+Hybrids at NBI or CERN
Fall-winter 2005	Bonding at CERN, system assembly and commission at
	NBI
February 2006	Ready to ship to CERN
June 2006	Ready to install in ALICE