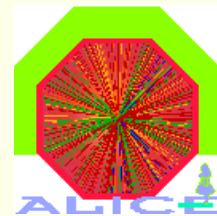


# Status of FMD

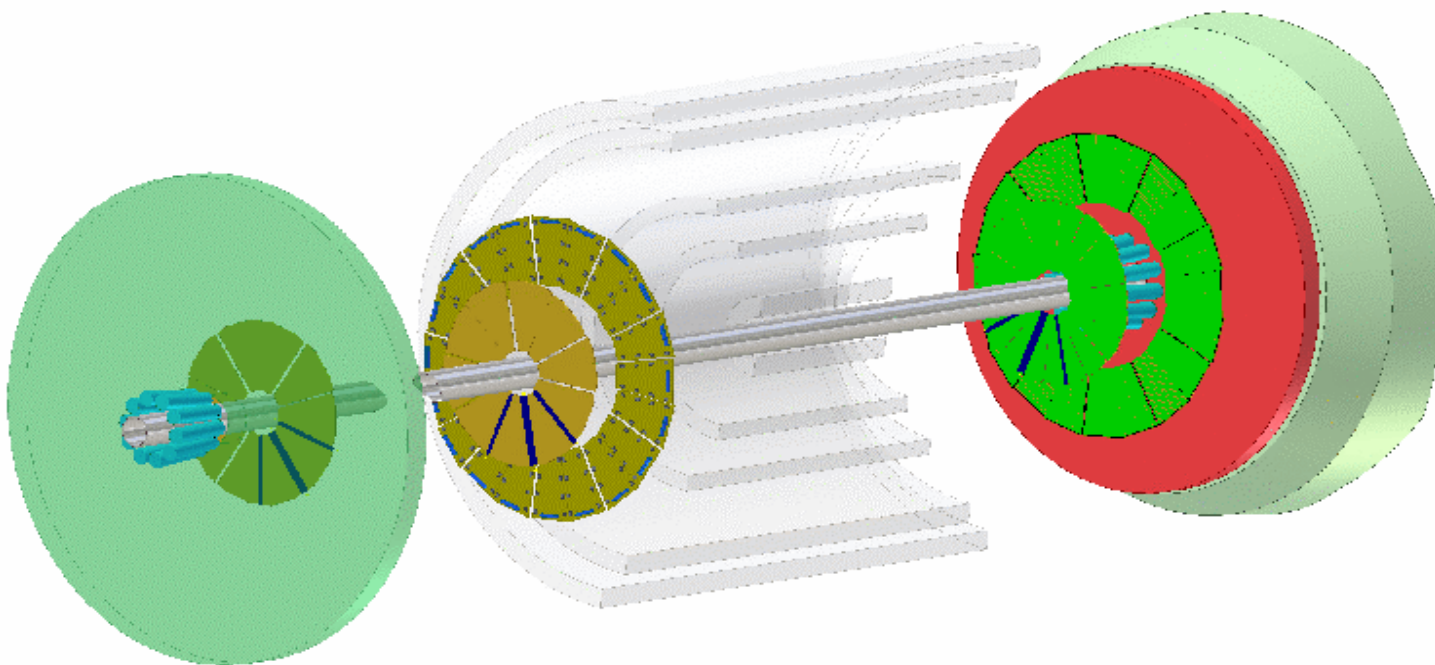
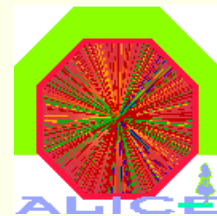


**Forward Detector Workshop**  
**CERN, 16 March 2004**

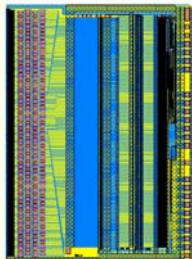
**Børge Svane Nielsen**  
***Niels Bohr Institute***

1. Integration and mechanical issues
2. Sensors
3. Preamp-shaper chips
4. Hybrids
5. Read-out strategy
6. Heat dissipation and cooling

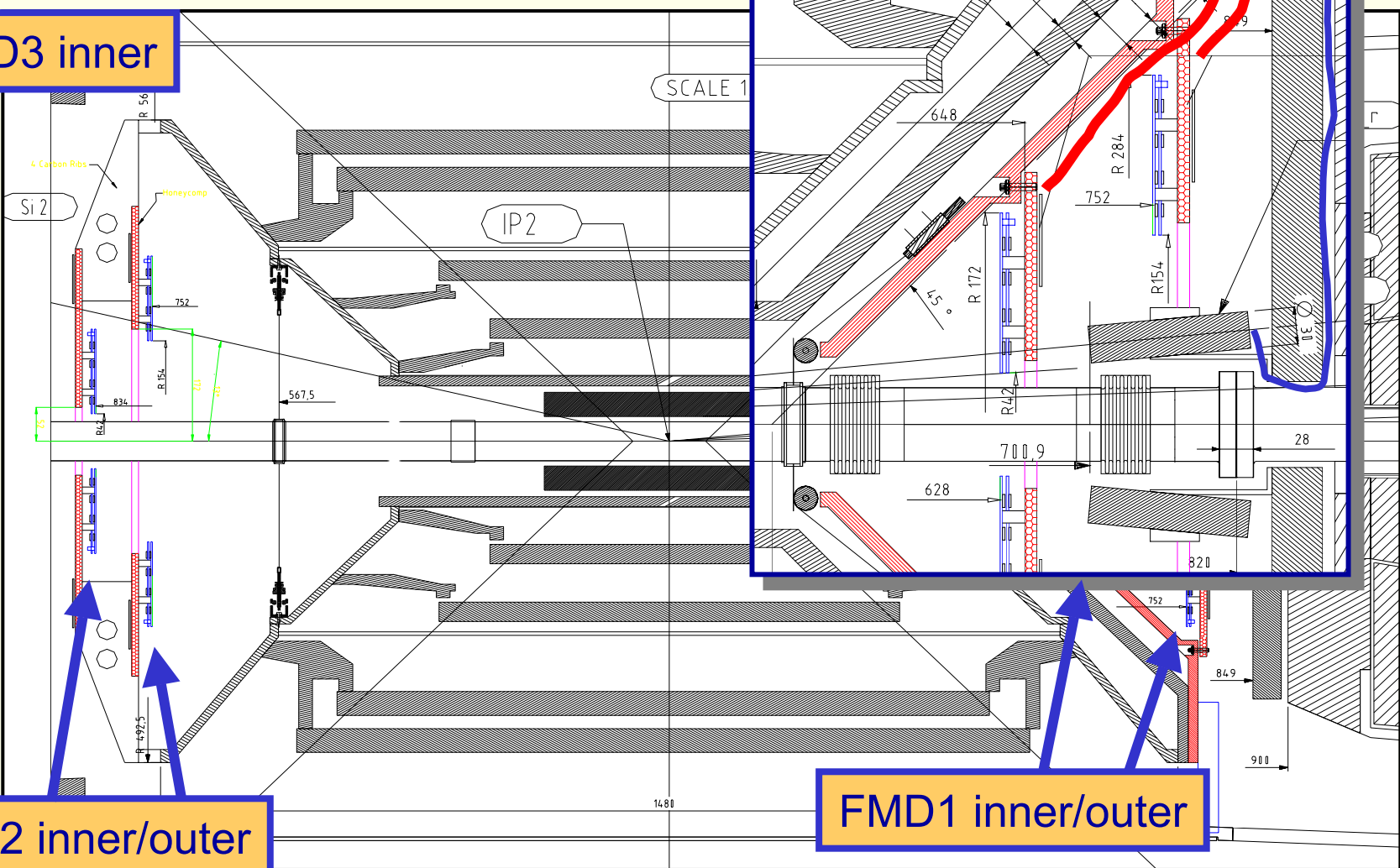
# Forward detector overview



# Integration



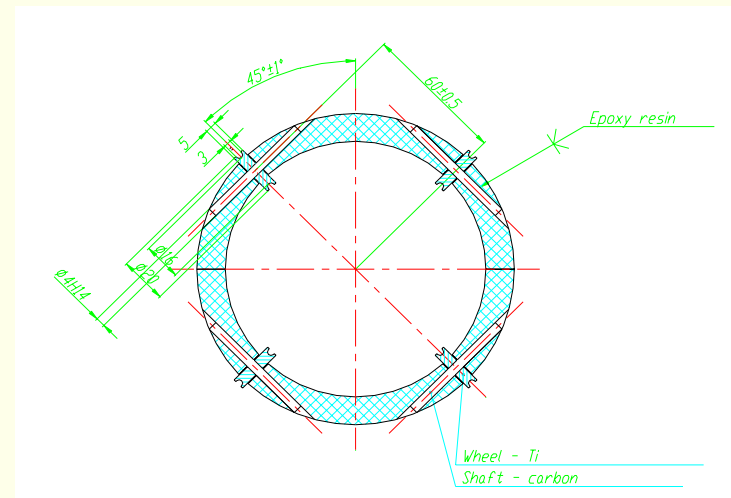
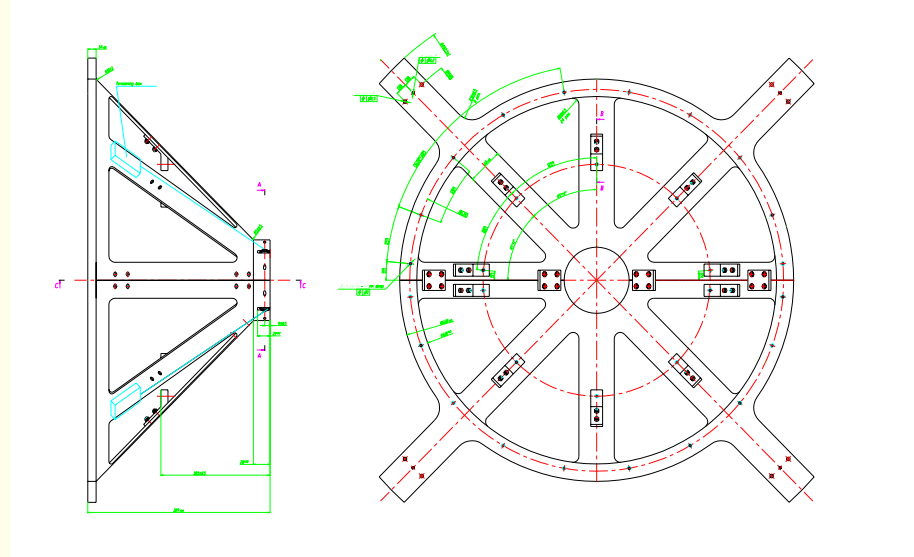
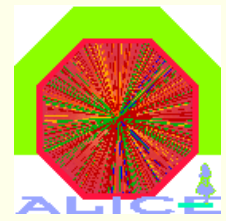
FMD3 inner



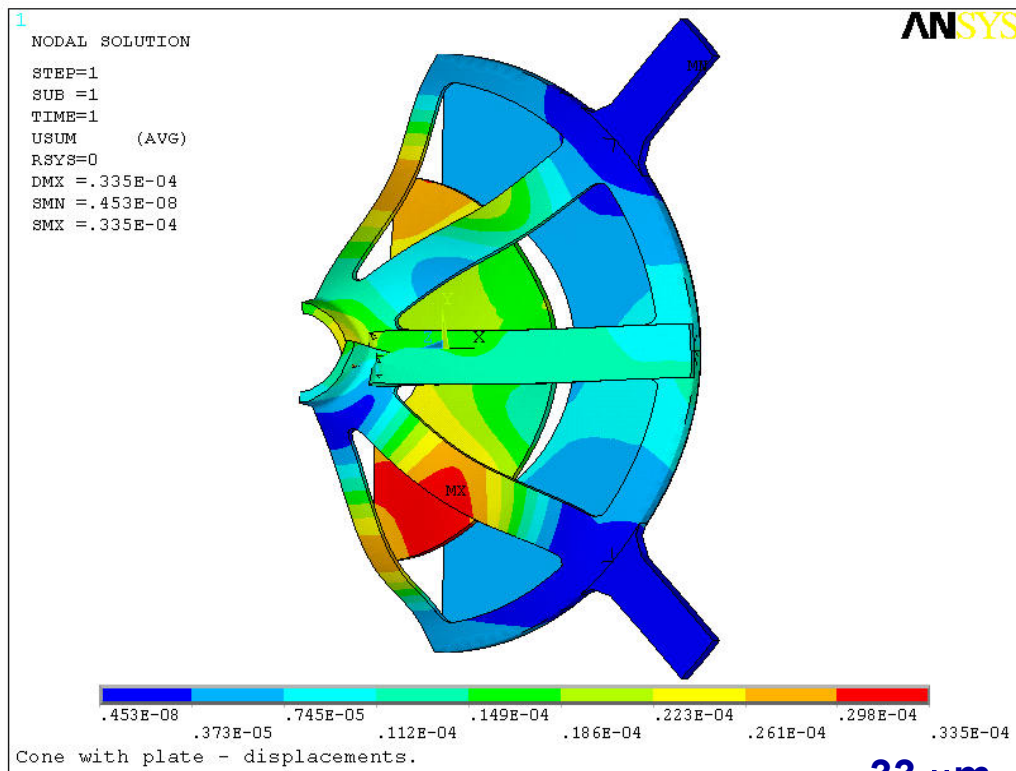
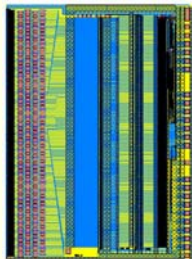
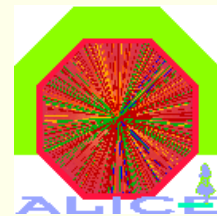
FMD2 inner/outer

FMD1 inner/outer

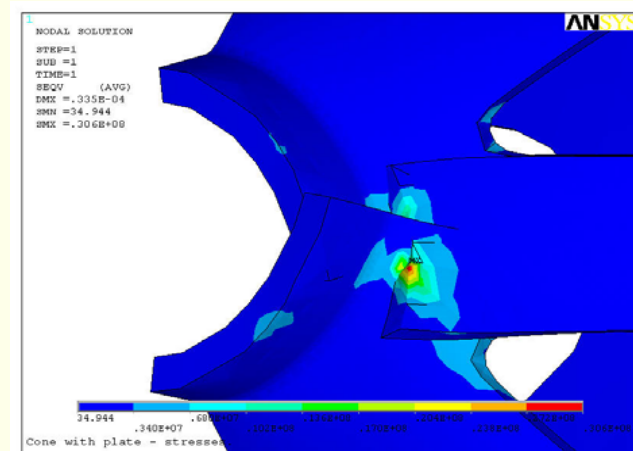
# Beam and FMD support cone RB26 side



# Finite element calculations of beam pipe support



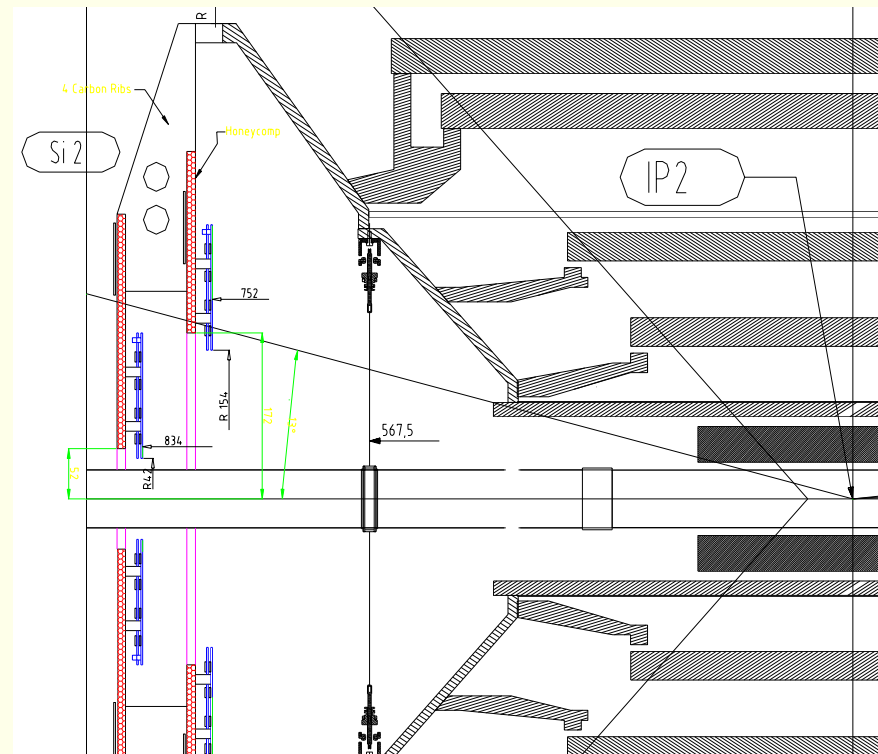
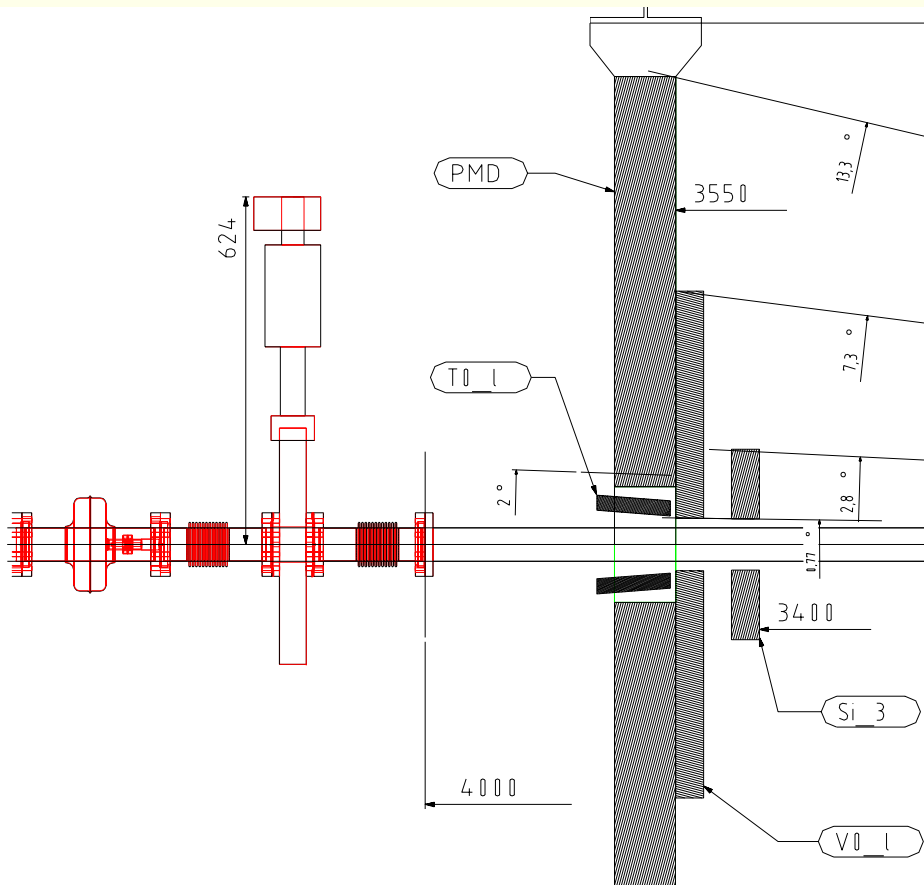
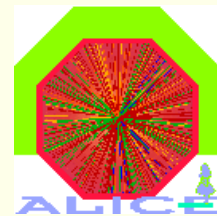
displacements



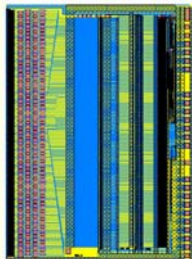
stresses

Prototype beam-pipe support structure (crucial for LHC) in construction at NBI

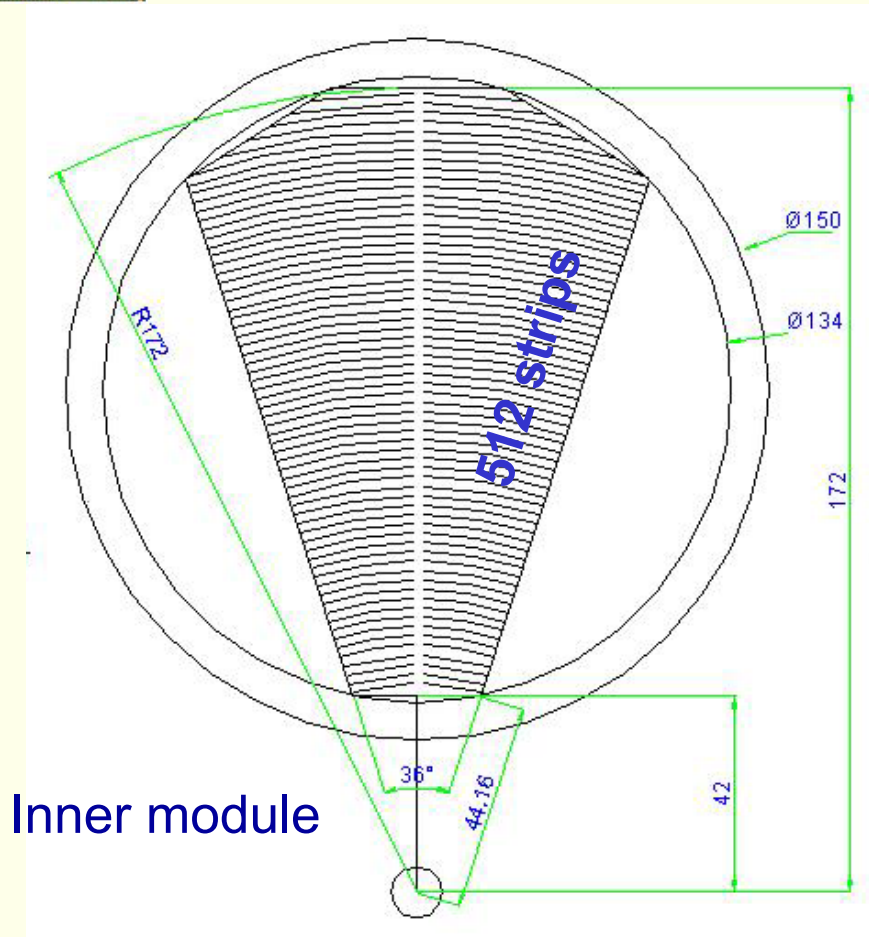
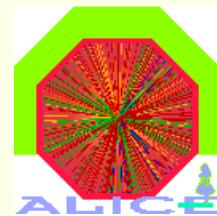
# RB24 Side: FMD2 & FMD3



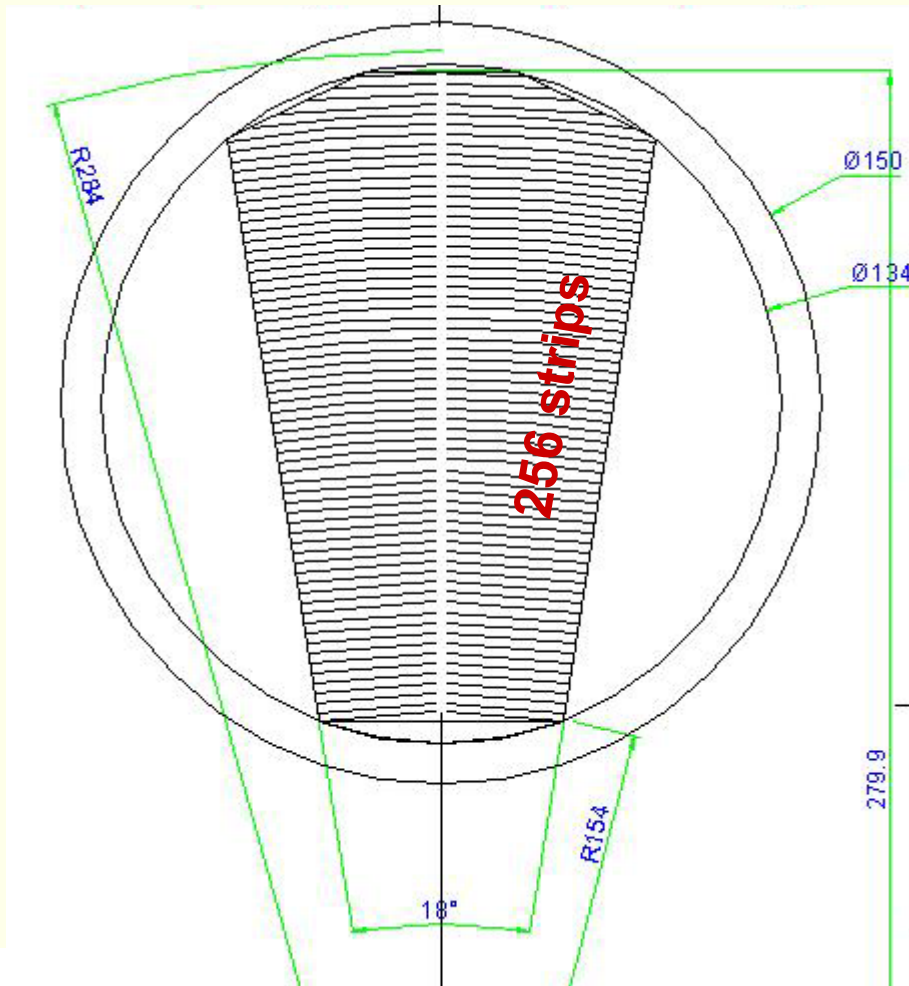
**Details of mounting not settled or designed**



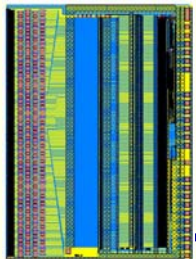
# FMD sensors



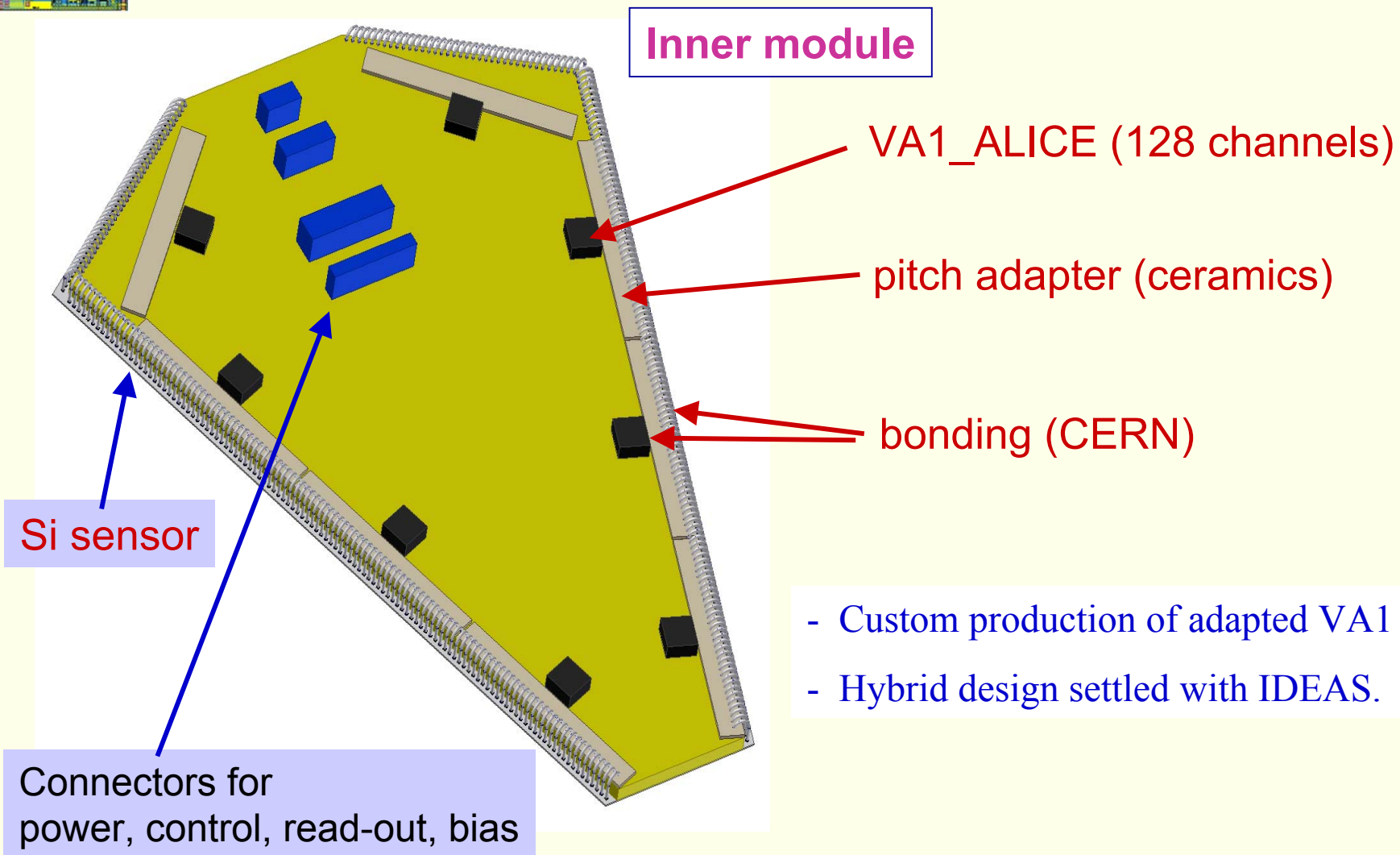
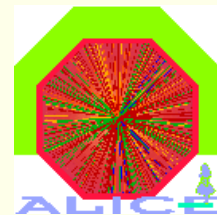
**Final negotiations with  
(Micron/Hamamatsu) ongoing.**



**Outer module**

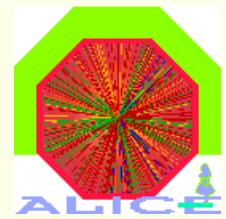


# Hybrids



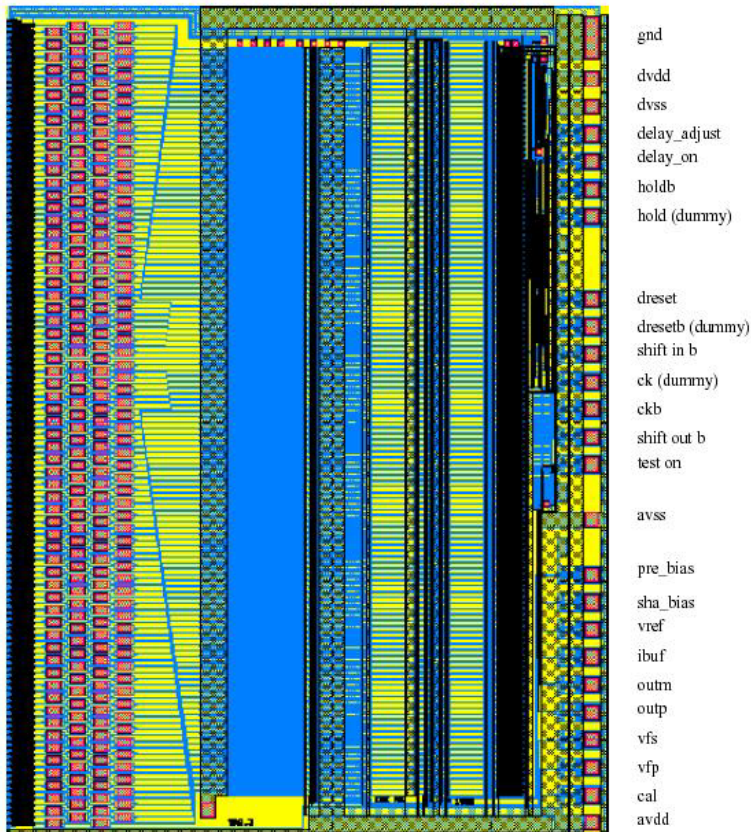


# Preamp-shaper chip



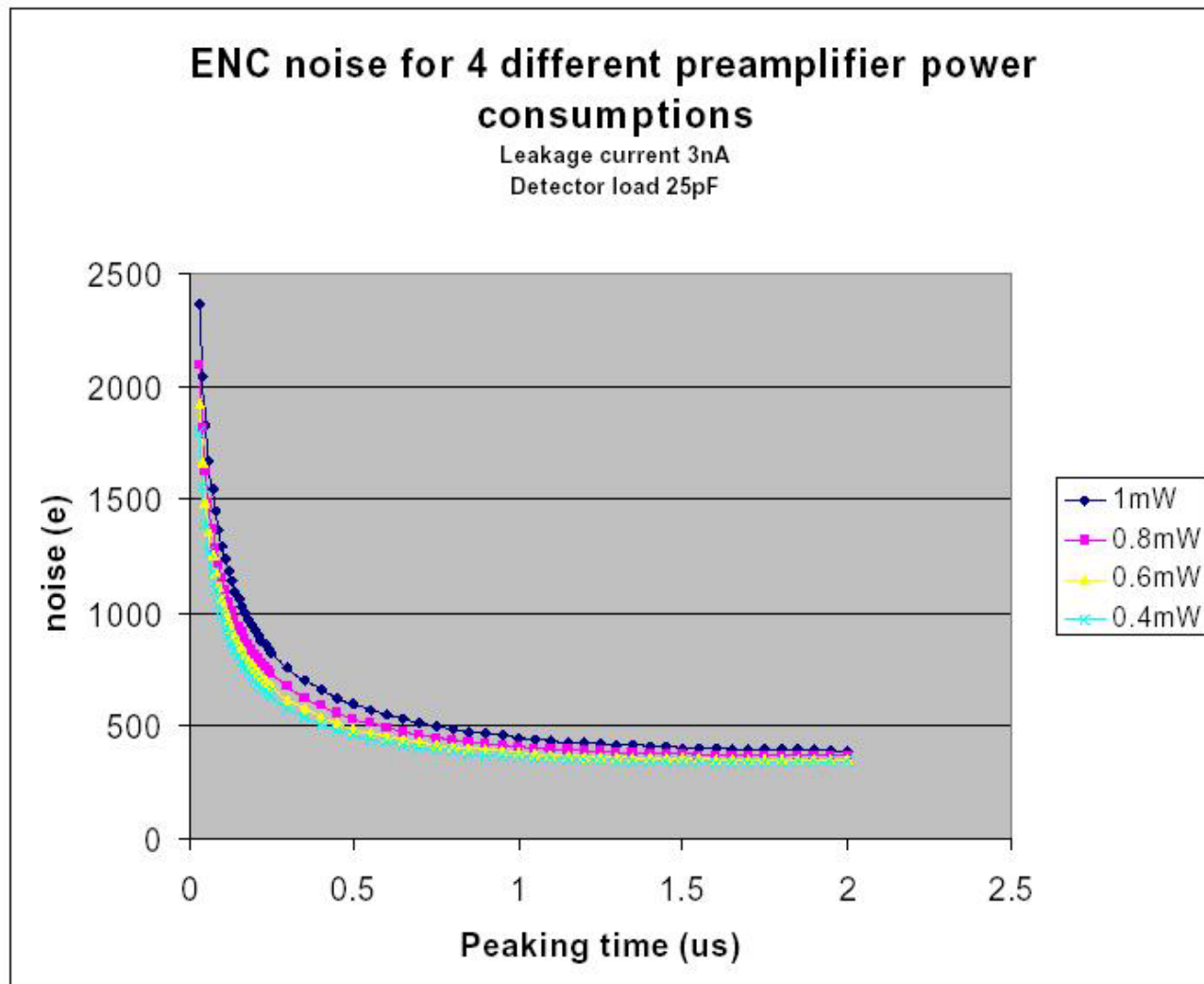
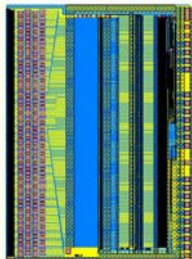
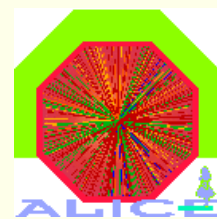
## VA1\_ALICE

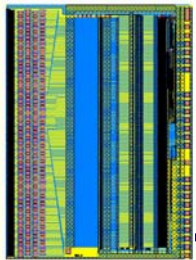
50  $\mu\text{m}$  input pitch



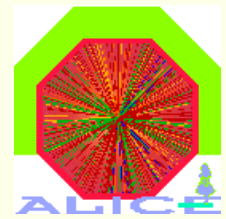
<b>Radiation hardness</b>	<b>&gt; 500-1000 krad</b>
<b>Peaking time</b>	<b>0.7-2.0 <math>\mu\text{sec}</math> (optimised at 1.2-1.4 <math>\mu\text{sec}</math>)</b>
<b>Noise (ENC)</b>	<b>&lt; 500 e- (= 0.02 MIP)</b>
<b>Capacitance matching</b>	<b>5-25 pF</b>
<b>Dynamic range</b>	<b>0-20 MIPs (or <math>\pm 10</math> MIPS)</b>
<b>Highly integrated</b>	<b>128 channels per chip</b>
<b>Read-out speed</b>	<b><math>\sim 10</math> MHz</b>
<b>Test and calibration circuits</b>	<b>included</b>
<b>Power consumption</b>	<b>0.6 mW per channel</b>
<b>Compatibility with ALTRO</b>	<b>requires level shift</b>
<b>Early prototype</b>	<b>VA1' useful</b>
<b>Affordable cost</b>	<b>yes</b>
<b>Channel count</b>	<b>51,200 (400 chips à 128 channels)</b>

# VA1\_ALICE noise simulations





# Radiation environment



Doses and Fluences in Central ALICE (10 years running):

FMD:

4.2 cm

28 cm

SSD2

TPC(in)

Radius [cm]	Dose [Gy]	Neutrons [cm <sup>-2</sup> ]	h-Φ [cm <sup>-2</sup> ]
3.9	2450	8.2E+11	3.5E+12
7.6	560	5.7E+11	1.3E+12
14	200	4.6E+11	5.5E+11
24	100	4.3E+11	3.2E+11
40	40	4.1E+11	2.3E+11
45	26	4.0E+11	2.0E+11
78	13	3.6E+11	1.5E+11

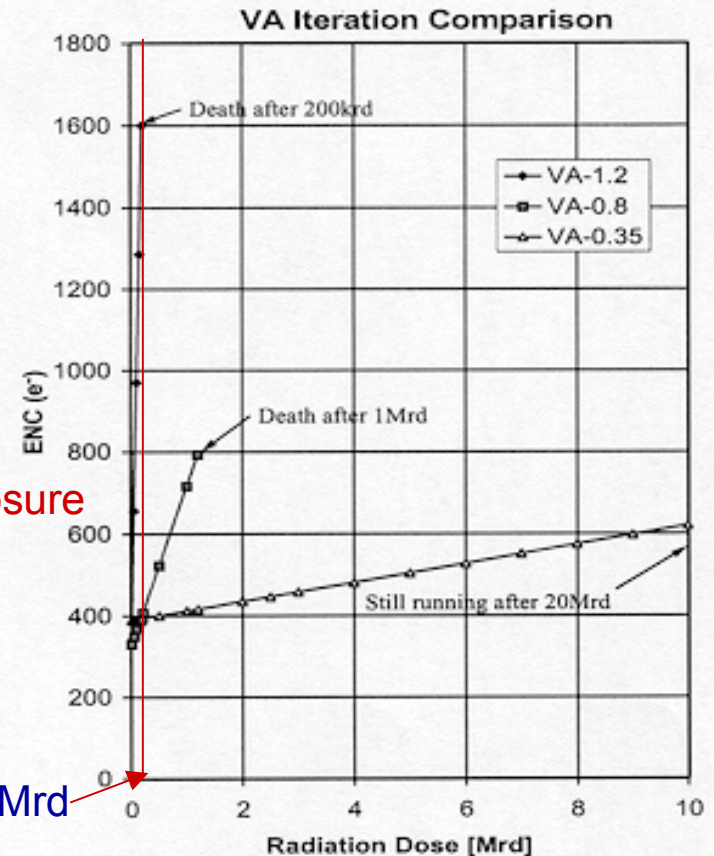
relevant for RCU exposure

Doses in FMD:

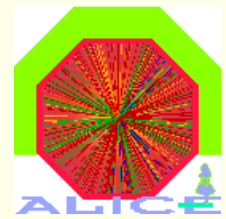
	Dose [Gy]	h-Φ [cm <sup>-2</sup> ]
FMD1	80-1300	8.6-13.0E+11
FMD2	40-2100	1.3-6.1E+11
FMD3	900-3100	2.2-5.2E+11

= 0.31 Mrd

Belle measurements



# Previous read-out strategy



## FMD Read-Out and Control Electronics

ON DETECTOR

IN CAVERN

IN COUNTING ROOM

FMD Module

Analog serial link  
(10 MHz)  
≤ 0.5 m

FMD Digitizer

Digital serial links  
(15-20 m)

FMD RCU

Detector Data Link  
(50-60 m)

VA read-out control

Trigger & Slow Ctrl

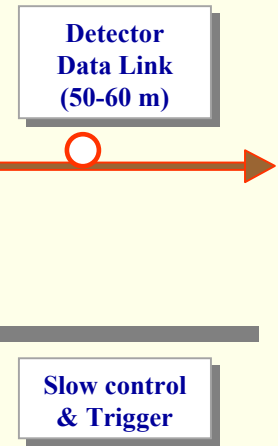
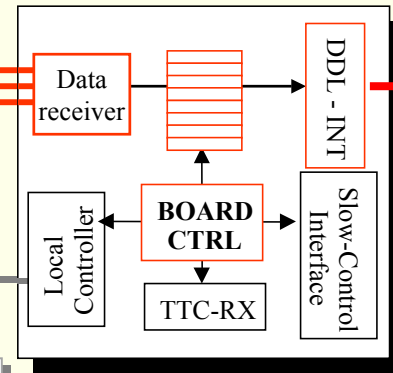
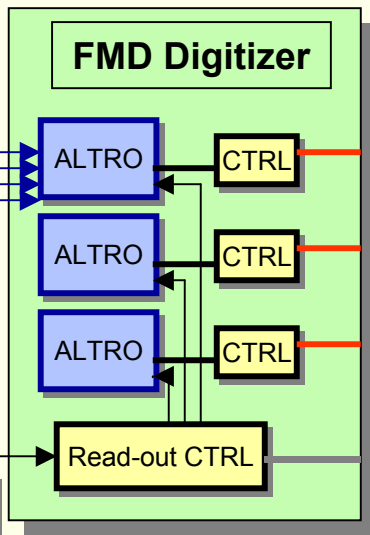
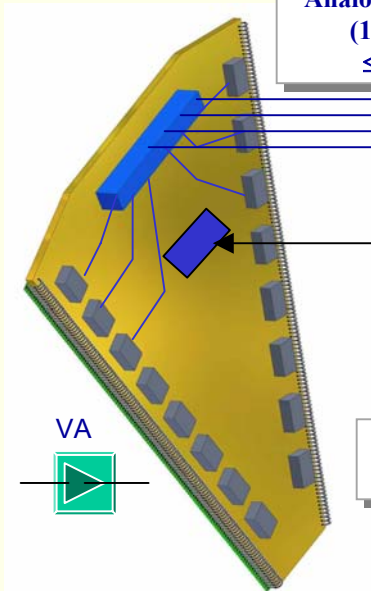
Slow control & Trigger

1 ring: 10/20 modules  
Full FMD: 70 modules

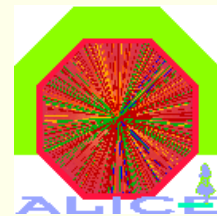
2 Digitizers  
10 Digitizers

1 RCU per side  
2 RCU's

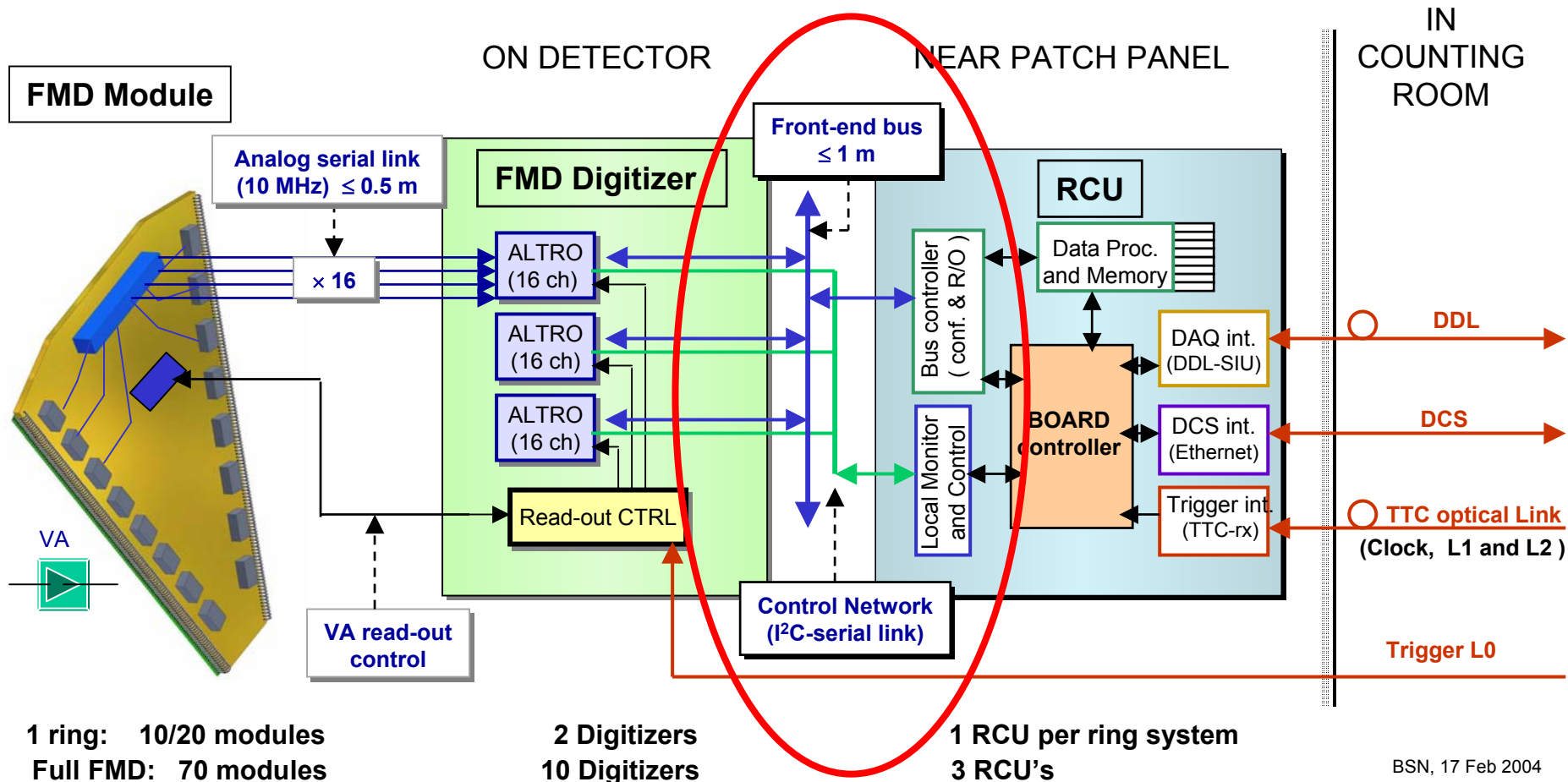
1 DDL per side  
2 DDL's



# New read-out strategy

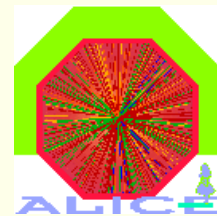


## FMD Read-Out and Control Electronics

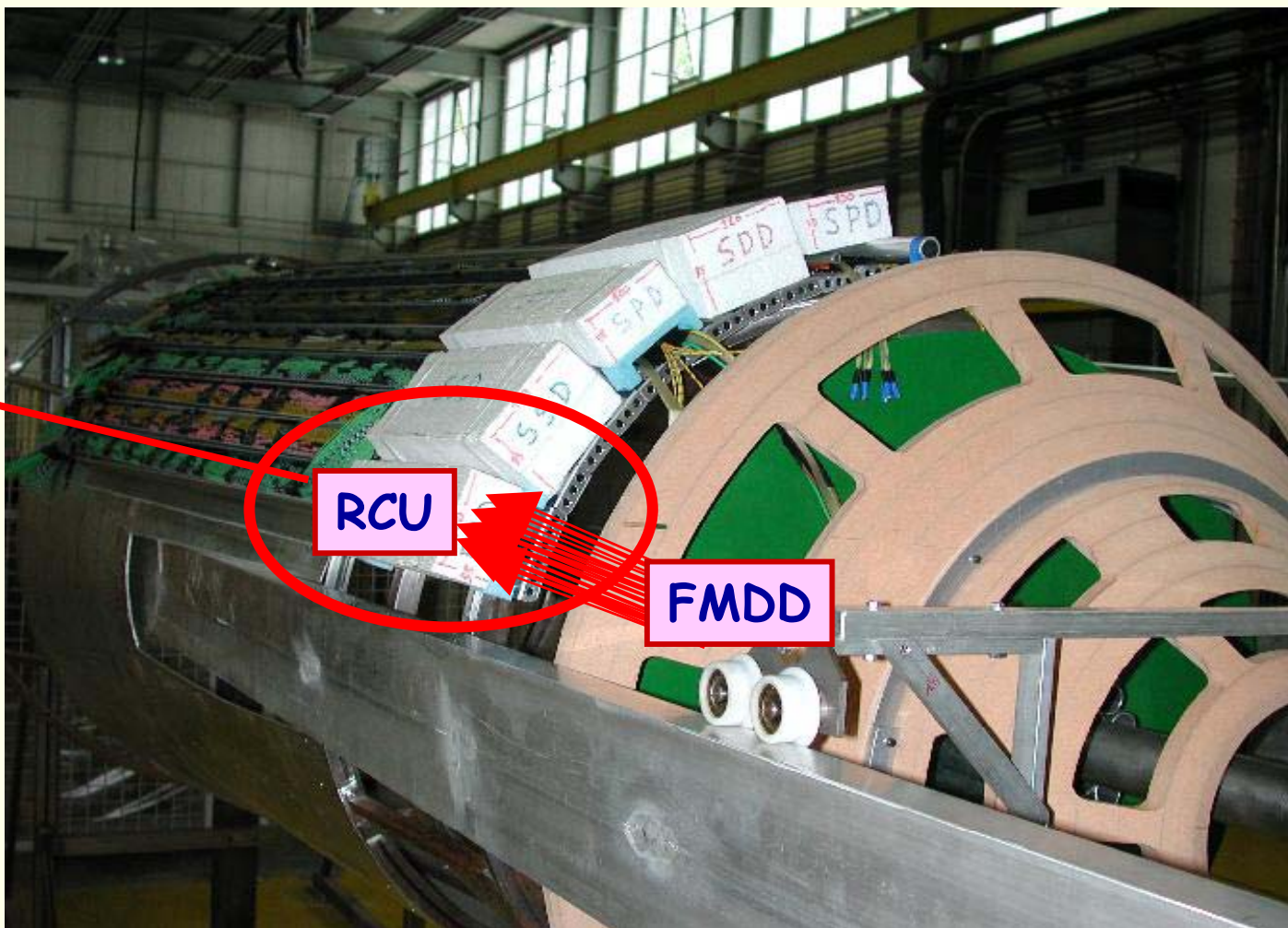


BSN, 17 Feb 2004

# Proposed new RCU location

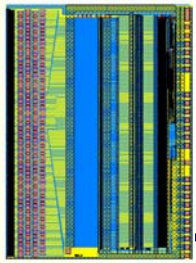


DAQ

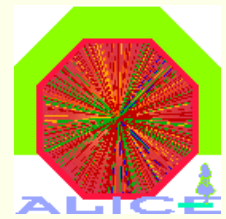


RCU

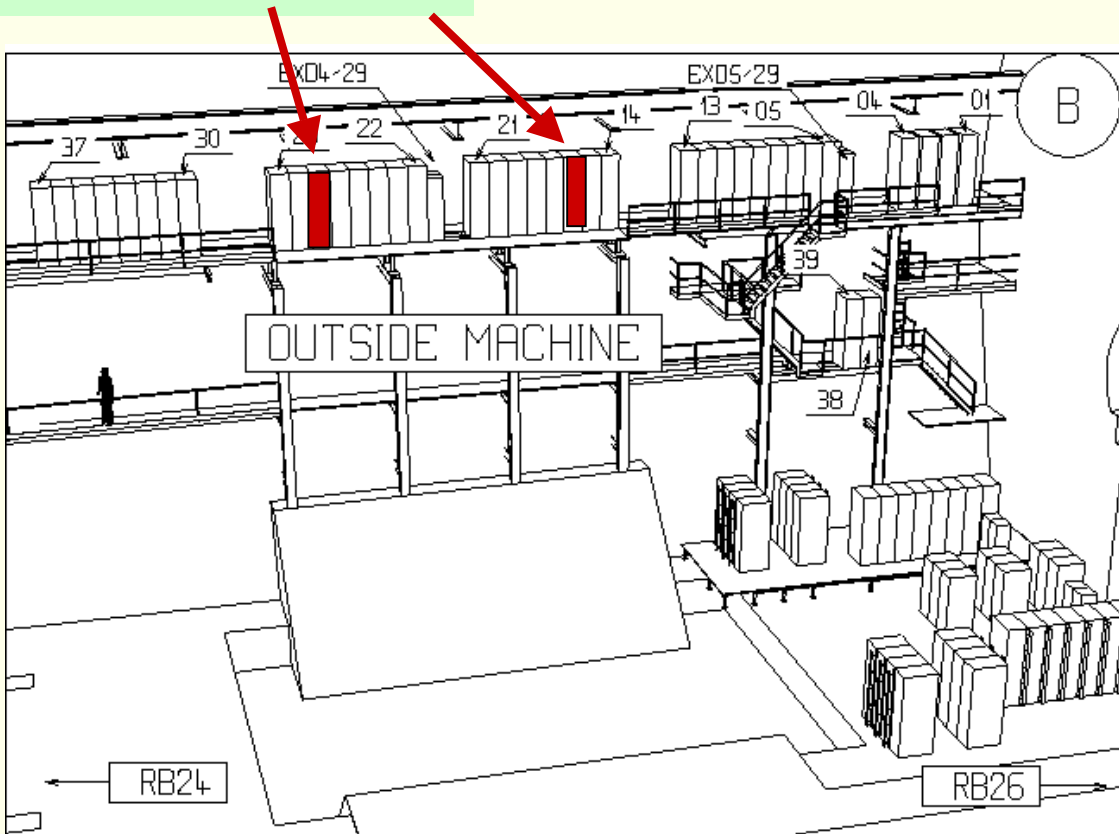
FMDD



# Rack allocation



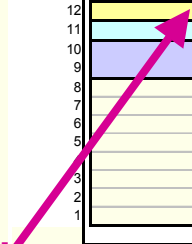
FMD racks: B27 + B16

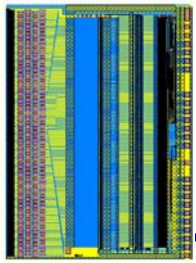


Rack Number: <b>A13</b>	
type: LHC-41	
Sub Detector-Function: <b>TPC-LV</b>	
JPV, 11/2/2004	

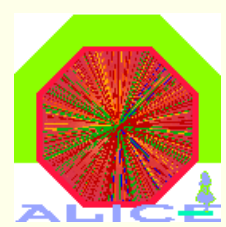
41	
40	
39	Ventilation + Control
38	
37	Heat Exchanger
36	
35	LV Crate 1
34	
33	
32	
31	LV Crate 2
30	
29	
28	Ventilation
27	Heat Exchanger
26	
25	LV Crate 3
24	
23	
22	
21	LV Crate 4
20	
19	
18	Ventilation
17	Heat Exchanger
16	
15	LV Crate 5
14	
13	
12	Ventilation
11	Heat Exchanger
10	Air Deflector
9	
8	
7	
6	
5	
4	
3	
2	
1	

Example (TPC) of rack space allocation:





# Heat dissipation



Heat dissipated by FE electronics of one FMD ring:

VA1-ALICE preamp chip (128 channels): 75-125 mW  
× 80 chips = 6-10 W / ring (previously 19 W)

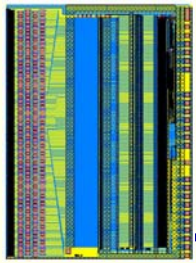
Read-out electronics and power distribution: 5-10 W / ring

## Concern:

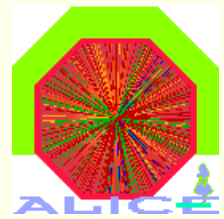
Temperature conditions inside cone not defined /unsatisfactory  
Recent temp. simulations indicate > 70 deg. C at FWD dets. location  
on RB26.

Substantial work on airflow etc. needed.





# Post Doc position at NBI



## Post Doc for the ALICE Silicon-Forward Multiplicity Detector Niels Bohr Institute

The High Energy Heavy Ion Group at the Niels Bohr Institute, University of Copenhagen, has a post doc position available to help in building the FMD.

We are seeking a person with documented expertise in Silicon detector design, construction and operation and experience in front-end and read-out electronics.

The position is available immediately. The appointment is formally for one year, with possibility of prolongation up to 5 years.

For further information contact: J.J. Gaardhøje, email: [gardhoje@nbi.dk](mailto:gardhoje@nbi.dk)  
or B.S. Nielsen, email: [borge@nbi.dk](mailto:borge@nbi.dk)

Applications, including CV, list of publications, names of 3 referees, details of past experience to:

J.J.Gaardhøje, Niels Bohr Institute, Blegdamsvej 17, 2100 Copenhagen, Denmark